

APPLICATION
FOR
UNITED STATES LETTERS PATENT

APPLICANT(S) NAME: Benson Chan
Mitchell S. Cohen
Paul F. Fortier
Ladd W. Freitag
Richard R. Hall
Glen W. Johnson
How Tzu Lin
John H. Sherman

TITLE: FIBER OPTIC CONNECTION AND
METHOD FOR USING SAME

DOCKET NO. EN999025

INTERNATIONAL BUSINESS MACHINES CORPORATION

Certificate of Mailing Under 37 CFR 1.10

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C., 20231 as "Express Mail Post Office to Addressee"

"Express Mail" Label Number	EL172581775
On	January 12, 2000
Denise M. Jurik	
Typed or Printed Name of Person Mailing Correspondence	
Denise M. Jurik	
Signature of Person Mailing Correspondence	1/12/2000 date

FIBER OPTIC CONNECTION AND METHOD FOR USING SAME

Field of the Invention:

The present invention relates to fiber optical connections and, more particularly, to a structure and 5 method for coupling a multiple channel fiber optic cable to a multiple channel Vertical Cavity Surface Emitting Laser (VCSEL) transmitter and a multiple channel Perpendicularly Aligned Integrated Die (PAID) receiver.

BACKGROUND OF THE INVENTION

10 The invention seeks to construct a package for coupling a multiple channel fiber optic cable to a multiple channel Vertical Cavity Surface Emitting Laser (VCSEL) transmitter and a multiple channel Perpendicularly Aligned Integrated Die (PAID) receiver. The active surface of both the 15 receiving and transmitting dies (hereinafter "optoelectronic dies") are oriented perpendicular to the plane of the laminate package. The package can be soldered directly to an end user card with its cables plugged directly through

the tail stock. In other words, the cable can exit from the card in a direction parallel to the plane of the card.

Other advantages of this design are:

- 1) it has integrated strain relief, a latching
5 detent, and safety features;
- 2) it uses available processes and materials;
- 3) it comprises a plastic ball grid array (PBGA)
10 laminate for high speed operation and low cost;
- 4) it has a two-part construction design with
independent testing of each part to improve overall yield;
- 15 5) it allows two strategies for removing heat from
the package;
- 6) it includes various incorporated features to
minimize electrical cross-talk, radiated RF and
susceptibility to external RF; and

7) it incorporates several features that serve to protect optical surfaces and electronic components from damage.

The development of this type of design has proven to be difficult, since solid state devices (often referred to either as dies or chips), with active components on one side only, are usually mounted parallel to the card, with their optically active features perpendicularly oriented to receive or emit light. It becomes necessary, therefore, to provide means for orienting the optoelectronic chips perpendicular to the card so that the emitted or received light enters the package parallel to the card while maintaining the profile (height above the card) low enough to meet specified limitations imposed by the end user.

15 Among the features incorporated into the design are:

1) an overmolded ball grid array (BGA) that strengthens and stiffens the relatively weak BGA laminate. (This is needed because the carefully aligned optics, which must be held in place, are integrated into the package);

2) a package having integrated strain relief for the cable/connector with direct mechanical coupling to the card, to prevent disturbing the optical portions;

3) use of relatively low cost materials, assembly 5 procedures and standard processes;

4) separate grounds within the BGA, a two part lid, a shroud and pins on the carriers for minimizing:

- 10 a) cross-talk from receiver to transmitter,
- b) radiated RF power, and
- c) susceptibility to RF pickup;

5) incorporation of many standard features of overmolded packages to minimize cost and susceptibility to damage;

6) an improved assembly/test strategy (to keep 15 yields high and costs low); and

7) a dual path strategy for removing heat from the package to keep the optoelectronic parts cool enough for high speed operation.

Discussion of Related Art:

There have been a number of attempts to develop a package and/or product that meets the general requirements of a parallel fiber optic link. A package developed for the 5 JITNEY project, a government funded package developed at IBM by M. S. Cohen et al., "Packaging Aspects of the Jitney Parallel Optical Interconnect," 1998 ECTC, pp. 1206-1215; and J. Crow et al., "The Jitney Parallel Optical Interconnect," 1996 ECTC, pp. 292-300, consisted of separate 10 transmitter and receiver modules and two separate cables to complete a bi-directional optical link. The cable contained 20 fibers for the simultaneous transmission of 20 channels of information at the full data rate. This design permitted the transmission of two bytes of information, along with 15 four bits of overhead, in each bit time. The transmitter module contained a driver chip (differential inputs) and a VCSEL transmitter. Both chips were mounted on a heatsink. The heatsink and chips were parallel to the module and the card.

20 A specially designed array lens served to redirect the light emitted from the VCSEL (perpendicular to the card) into the input face of the fibers which were disposed

parallel to the card. Similarly, the receiver module contained a receiver chip (generating differential outputs) also oriented parallel to the module. The same array lens was used to redirect the light emerging from the optical 5 fibers into the direction perpendicular to the card, so that the light to be detected impinged upon the surface of the receiver substantially perpendicular to the photosensitive surface of the receiver. Although several similarities may be found in the JITNEY package, it did not attempt to solve 10 the crosstalk problem (transmitter to receiver). It used two separate modules. JITNEY had a separate strain-relief, used a leadframe (instead of a BGA), and generally did not use packaging techniques expected to support data transmission rates of 1 Gigabit per second per channel or 15 more.

Two versions of link transceiver modules developed for the Motorola Optobus project are described in two papers: "Characteristics of VCSEL Arrays for Parallel Optical Interconnects," 1996 Electronic Components and Technology Conference (ECTC), pp. 279-291; and "Optobus I: A Production 20 Parallel Fiber Optical Interconnect," 1997 ECTC, pp. 204-209. Both modules have in common a number of packaging features: 1) a surface emitting VCSEL array is used, the

light path is parallel to the host card plane, and the optoelectronic component is mounted perpendicular to the module's BGA laminate, 2) a molded plastic waveguide structure conducts the light to/from the optoelectronic die 5 to endfaces of the fibers in ribbon optical cables, the cables being terminated with MT connectors, 3) a glob encapsulated, multi-chip pin grid array laminate board is provided on which the optoelectronic subassembly is mounted, and 4) the resulting package is nonhermetic.

10 As fabricated, the loss of the low-loss waveguides is some tenths of a dB/cm. To meet safety goals and to increase the amount of optical power reaching the detector, the waveguides for the transmitter and for the receiver portions of this transceiver are not constructed 15 identically. On the transmitter side, the waveguide is designed to increase the numerical aperture of the entering beam as the light passes from the VCSEL to the optical fiber. On the receiver side, the waveguide is designed to improve coupling efficiency from the optical fiber to the 20 photodetector. A passive alignment procedure (i.e., the optical elements are not electrically activated during the procedure) is used to align the array of optically active elements on the optoelectronic dies to the molded structure

which contain an array of waveguides.

In the Optobus transceiver (the 1996 paper), a leadframe for delivering the electrical signals to the optoelectronic die is overmolded and serves as the 5 supporting structure for the waveguide arrays. Electrical connections are made from finished ends of the leadframe conductors, a nonstandard packaging approach, to contact pads on the top surface of the optoelectronic dies. The other end of each of the leadframe's conductors, which are 10 routed to exit points along the side of the molded waveguide structure and then bent down to attachment points, are electrically connected to pads on the top surface of the laminate board.

In the Optobus I transceiver (the 1997 paper), a tape 15 automated bonding (TAB) leadframe is used to replace the electrical function of the standard leadframe in the earlier version. The conductors on one end of this TAB leadframe make electrical contact to the optoelectronic dies and, on the other end, to contacts on the top surface to the 20 laminate board. The TAB leadframe is bent 90 degrees between the two ends. Again, alignment between the molded waveguide structure and the optoelectronic dies is

accomplished using a passive alignment technique.

The PAROLI (Siemens) project, "Parallel Optical Link for Multichannel Gigabit Rate Interconnections," H. Karstensen et al., 1988 ECTC, pp. 747-754, comprised an optical coupler which redirected the light 90 degrees. An array of multimode fibers was captured in a transfer-molded holder. The end of the fiber array was polished at an angle, and the optically active chips were die bonded in position below the polished facets, so that the light followed the desired path. Active alignment was used to position the chips in the facets. The optical coupler mated to an MT type of optical connector. Nonhermetic packaging was used.

To make the link, two separate 12-channel modules were used, one for the transmitter and one for the receiver. An AC coupled link was built with a data rate of 1 Gbit/s for each channel; a DC coupled link was built with a data rate of 500 Mb/s for each channel.

A PARABIT (NTT) 40-channel parallel optical interconnection module with a throughput in excess of 25 Gbit/s, K. Katsura et al., "Packaging for a 40 channel

Parallel Optical Interconnection Module with an Over 25-Gbit/s Throughput," 1998 ECTC, pp. 755-761, comprised a transceiver consisting of 20 transmitting and 20 receiving channels in one module, and utilized a multimode fiber. A 5 250mm pitch was used, with 850 μ m VCSELs and GaAs pin photodiodes. Light was carried to and from the optically active chips by means of polymeric waveguides, which were cut at 45 degrees in order to redirect the photon path. Passive alignment of the chips to the waveguides was used, 10 where the principle of index alignment was employed. A unique "bare-fiber" connector was used to couple the waveguides to the fibers. There, bare fibers protruded from the end of the connector. The fibers were inserted into micro capillaries, which were mated to the polymeric 15 waveguides. The fibers were buckled to create a constant force on the ends of the waveguides, in order to maintain good physical contact.

In the POLO 980nm DuPont project, by K. Hahn et al., "Gigabyte/sec Data Communications with the POLO Parallel 20 Optical Link," 1996 ECTC, pp. 301-307, a bottom-emitting VCSEL array chip was used with a PIN photodiode. These chips were incorporated into two widely spaced sub-modules, each of which had 10 channels. Each channel was projected

to operate at 1 Gbit/s. The optical coupler which transmitted light to and from the MT connector was based on polymer waveguides, and consisted of commercially available "Polyguide" (DuPont) waveguides. A 90° redirection in the 5 light path was accomplished by providing a 45° bevel in the end of the polyguide, thereby forming a mirror. The optically active chips were first diebonded, before the polyguide was aligned.

10 A Hitachi package by A. Miura et al., "Reliable, Compact, CMOS Interface, 200 Mbit/s X 12-channel Optical Interconnects Using Single-Mode Fiber Arrays," 1997 ECTC, pp. 225-230; and A. Takai et al., "200 Mb/s/ch 100-m Optical Subsystem Interconnections Using 8-Channel 1.3-mm Laser Diode Arrays and Single-Mode Fiber Arrays," J. Lightwave 15 Tech., vol. 12, pp. 260-270, 1994, comprised 200 Mb/s X 12 channel optical interconnects using single-mode fiber arrays. The Hitachi modules, which are separate transmitter and receiver modules, were designed for long wavelength single-mode operation. For this purpose 1.3 μ m edge-emitting lasers were used, together with full hermetic 20 sealing. Planar microlens arrays were employed for light coupling. An array of 12 channels was used for both the transmitter and receiver modules. Each channel ran at 200

Mbit/s. The fibers were placed in silicon V grooves for accurate placement. Alignment was accomplished by first roughly aligning using stereo microscope image processing, and accurate adjustment was then accomplished using computer controlled, two-dimensional scanning.

A 3.5 Gbit/s X 4 ch Interconnect (NTT), by N. Tanaka et al., "3.5 Gb/s x 4 ch Optical Interconnection Module for ATM Switching System," 1997 ECTC pp. 210-216, provided a structure for a multichannel fiber optic package using silicon V-groove technology to provide a 4-channel fiber array with the fibers encased in micro-capillaries. The fiber ends were hemispherically lensed. Passive alignment was used by bonding the edge-emitting laser to the same silicon substrate which held the fibers. The same principle was used for the photodiode array, but the fiber lenses were slant polished in this case. In both cases a fiber pitch of 250mm was used. Each channel was operated at 3.5 Gbit/s.

A one Gbyte/s Array (NEC), by T. Nagahori, "1-Gbyte/sec Array Transmitter and Receiver Modules for Low Cost Optical Fiber Interconnection," 1996 ECTC pp. 255-258, comprised silicon V grooves used in conjunction with edge emitting lasers. The laser was passively aligned to the silicon

substrate by solder-bump technology, as was the monitor photodiode. No lenses were used there. The light was redirected 90 degrees to the photodiode by etching a slope on the silicon submount and metallizing. A $1.3 \mu\text{m}$ wavelength was used. Eight channels operating at 200 Mb/s each gave a 1 Gb/s throughput.

In the POINT project (GE, Amp, Honeywell, Allied Signal), Y. S. Liu et al., "Plastic VCSEL Array Packaging and High Density Polymer Waveguides for Board and Backplane Optical Interconnect," 1998 ECTC, pp. 999-1005, a plastic VCSEL array package was provided with high density polymer waveguides, for a board and backplane optical interconnect. POINT was a demonstration project to show feasibility of low cost VCSEL and receiver array packaging. There, a GE process was used to attach the optically active chips to a polymer film on which conductor lines had previously been defined. Next, epoxy encapsulant was introduced in order to impart good mechanical stability to the structure. A polymer film was fabricated over the chips. This polymer film was patterned by a precision laser micromachining system in order to fabricate passive alignment features into the film. These alignment features were keyed to fiducial marks on the chips. The alignment features were used for

passive alignment of polymer waveguides to the optically active chips. The system permitted 10 channels each for the transmitter and receiver sides of the package. The modules were apparently not given full functional tests.

5 Another package was developed by OETC (Lucent, IBM, Honeywell, Univ. Minnesota, Univ. Illinois), Y. M. Wong et al., "Optoelectronic Technology Consortium Parallel Optical Data Link: Components, System Applications, and Simulation Tools," 1996 ECTC, pp. 269-278.

10 Yet another package (Hewlett Packard Laboratories and Univ. of North Carolina) was described by P. Rosenberg et al. in a paper entitled "The PONI-1 Parallel-Optical Link", 1999 Electronic Components and Technology Conference, IEEE, pp. 763-769. The PONI devices separated the receiver and transmitter functions, and did not disclose a transceiver device, nor a faraday barrier, which permits the close proximity of the transmitting and receiving functions. No means was disclosed to allow the pins to be prealigned before final accurate engagement occurs. The PONI device
15 permitted pin engagement of an MT connector directly to a metal heatsink/base. The PONI alignment scheme was
20 "passive", in that electrically active optoelectronic

components were not energized to assist in the alignment process. An electronic chip was mounted close to the optoelectronic device, both chips mounted perpendicularly relative to the board on which the PONI package was mounted, 5 significantly raising the height profile of the overall package. Because the chips were located in close proximity to one another, heat removal from the sensitive optoelectronic part was problematical, since the more heat tolerant electronic device required heat removal, as well. 10 That is, removing heat from the relatively heat tolerant device had to be accomplished without damaging the more sensitive optoelectronic part.

Some of the differences of the current invention over the aforementioned art are as follows:

15 The package of this invention does not use polymer films which serve as waveguides anywhere in its construction. The use of optically transparent media spanning the distance from the optoelectronic chip to the optical coupler is anticipated, but this is not a light 20 guiding structure.

The invention uses an array of fiber stubs to form the

optical coupler.

The invention does not use silicon bench (silicon V groove) technology in its module except, possibly, in the construction of the optical coupler portion of the module.

5 Silicon bench technology may be used in the fabrication of the ferrule portion of the connector used to terminate the array of fibers in the cable.

The invention does not use self-aligning solder-bump attachment techniques within the module.

10 The invention is a transceiver module, using surface emitting rather than edge emitting lasers, and does not use microlenses.

The invention does not use leadframes.

15 The preferred fabrication approach uses active alignment.

In United States Patent No. 5,420,954, issued to Swirhun et al on May 30, 1995 for PARALLEL OPTICAL INTERCONNECT, an optical interconnect that couples multiple

optical fibers to an array of optoelectronic devices, is
illustrated. The patent describes a parallel connection
between the chip and the fiber cable. The connection of
these parts are very close, but it is not desirable to have
5 a removable part so close to sensitive electronic parts.

In United States Patent No. 5,818,994, issued to
Hehmann, on October 6, 1998 for DEVICE FOR THE UNADJUSTED
COUPLING OF A NUMBER OF OPTICAL WAVEGUIDES TO A LASER ARRAY,
a coupling of optical fibers to a laser array is shown. The
10 patent teaches a non-removable connection between the
optoelectronics and the optical fiber array.

In United States Patent No. 5,832,150, issued on
November 3, 1998 to Flint, for SIDE INJECTION FIBER OPTIC
COUPLER, a coupler is illustrated that couples an
15 asymmetrical beam of a laser diode into a fiber optic cable.
The device depicts a reflective end face to reflect laser
radiation. The input facet is approximately parallel to the
central axis of the array.

In United States Patent No. 5,631,988, issued on May
20, 1997 to Swirhun et al for PARALLEL OPTICAL INTERCONNECT,
an optical interconnect is shown that couples multiple

optical fibers to an array of optoelectronic devices with a parallel orientation.

In United States Patent No. 5,121,457, issued to Foley et al on June 9, 1992 for METHOD FOR COUPLING LASER ARRAY TO 5 OPTICAL FIBER ARRAY, a method is disclosed of using V-grooves to align individual fibers in precise relationship to their light emitting devices. The fibers have polished end face facets at forty-five degrees, which are orthogonally attached to a mating configured surface.

10 In United States Patent No. 5,454,814, issued on November 12, 1996 to Noddings et al for PARALLEL OPTICAL TRANSCEIVER LINK, an optical interconnect module is shown that mates with an optical fiber connector having a parallel orientation. The link comprises a sapphire window having 15 metallized features for conducting electronic signals to a VCSEL.

20 In United States Patent No. 5,781,682, issued to Cohen et al on July 14, 1998 for LOW COST PACKAGING FOR PARALLEL OPTICAL COMPUTER LINK, a coupling apparatus is shown for coupling a connector of a parallel optical cable to a receiver or transmitter array.

In United States Patent No. 5,774,614, issued on June 30, 1998 to Gilliland et al for OPTOELECTRONIC COUPLING AND METHOD OF MAKING SAME, a coupling is depicted that provides an optoelectronic device to be attached and aligned with a 5 flexible substrate whose end face is mounted to an optical waveguide, similar to that shown for the present invention. The flexible waveguide provides the ability to orient the connection. The present invention departs from this patent by incorporating strain relief latching, RF isolation, 10 reduction in electrical cross-talk, a heatsink, and fabrication with a protective overmolding.

In United States Patent No. 5,432,630, issued on July 11, 1995 to Lebby et al for OPTICAL BUS WITH OPTICAL TRANSCEIVER MODULES AND METHOD OF MANUFACTURE, a transceiver 15 module is shown which includes both semiconductor devices and optoelectronic devices, as well as coupling features for attaching a parallel cable. The optical coupler is an array of molded waveguides, not an optical coupler made of an array of fibers. No overmold is used, nor are details 20 provided regarding how strain relief or any prealignment function is accomplished. No disclosure of back side electrical contact to the light generating or light receiving devices is made. No means are provided by which

heat is removed from the various heat generating elements. This would be important, especially at high operating speeds. No flexible circuit is disclosed for conducting current from the top surface of the laminate to the 5 optoelectronic chips.

SUMMARY OF THE INVENTION

In accordance with the present invention, there is provided a single package for coupling a multiple channel fiber optic cable to a multiple channel Vertical Cavity 10 Surface Emitting Laser (VCSEL) transmitter and for coupling a second multiple channel fiber optic cable to a multiple channel Perpendicularly Aligned Integrated Die (PAID) receiver. The active surface of both the receiving and transmitting (optoelectronic) dies are oriented 15 perpendicular to the plane of the lamine package. The package can be soldered directly to an end user card, and have its cable plugged directly through the tail stock. In other words, the cable can exit from the card in a direction parallel to the plane of the card.

20 The package article comprises a laminate table or board

(hereinbelow referred to simply as a laminate) upon which amplifier dies, preferably attached by wirebonds or other attachment means known in the art, are supported. The laminate carries an overmold frame that houses, optionally, 5 a faraday barrier shield for RF isolation purposes. The overmold frame supports an optical subassembly which accepts an optical connector that is attached to an end of the parallel fiber optic cable. A retainer substantially encloses an optical coupler. Attached to the optical 10 coupler is a heatsink carrier which, in turn, supports an optoelectronic die. One function of the heatsink carrier is to remove heat from the optoelectronic die. The heat drawn into the heatsink carrier may be dissipated into the nearby air. Optionally, the heat may pass through a heat 15 conducting compound to a package cover where it is then dissipated to the air.

20 Electrically attached to the optoelectronic die is a flexible circuit. The flexible circuit itself can be electrically attached to the heatsink carrier. The flexible circuit is mechanically supported by the heatsink carrier using an adhesive. One purpose of the flexible circuit is to reorient the electrical signals from pads on the surface of the laminate to pads on the optoelectronic die, which is

substantially perpendicular to the plane of the laminate.

Optoelectronic elements on the optoelectronic die are aligned with the optical coupler and end faces of the optical fibers in the connectors, so that optical signals 5 pass from the optoelectronic elements to the optical fibers. The space between the optoelectronic die and the nearby end face of the optical coupler is optionally filled with a substantially transparent material. This transparent material also serves as a weak mechanical link between these 10 two elements. Shock forces applied to the optical coupler are thus prevented from adversely affecting the optoelectronic die. The transparent material also assists in protecting the optoelectronic die from environmental sources of contamination.

15 An optical coupler subassembly consists of the optical coupler, the heatsink carrier, the optoelectronic die and the flexible circuit. The optical coupler is secured to the heatsink carrier on one distal end thereof. The optical connector, secured to one end of a parallel fiber optic cable, is partially contained within a receptacle in the 20 retainer and held by a latch mechanism against the other distal end of the optical coupler. The optical coupler

comprises an array of optical fibers mounted substantially within a protective housing. The end faces of the protective housing and the optical fibers are prepared so that the end faces of the optical fibers have an optical
5 finish.

The optical subassembly consists of the optical coupler subassembly attached to the retainer. Specifically, the optical coupler portion of the optical coupler subassembly fits into a receiving bore in the retainer. The retainer has mechanical features which serve to align it to similar
10 complementary features in the overmold frame. In a preferred embodiment, the retainer and optical coupler subassembly are adhesively attached to each other.

The package article can be a transceiver on a single
15 laminate for receiving and transmitting.

Objects of the Invention:

It is an object of this invention to provide an improved coupling assembly between a horizontally oriented fiber optic cable, and a vertically oriented optoelectronic

die.

It is another object of the invention to provide a method for accepting signals from a host card and generating corresponding high-current signals for transmitting to an 5 optoelectronic die; and for accepting low-current signals generated by the optoelectronic die and amplifying and digitizing the received signals to levels suitable for transmitting to the host card.

It is yet another object of the invention to provide a 10 transceiver that has a vertically oriented die and that accepts a horizontally oriented parallel fiber optic cable, wherein forces applied by the optical connector to externally accessible parts of the package article are substantially mechanically isolated from the optoelectronic 15 die.

BRIEF DESCRIPTION OF THE DRAWINGS

A complete understanding of the present invention may be obtained by reference to the accompanying drawings, when considered in conjunction with the subsequent detailed

description, in which:

FIGURE 1 illustrates a perspective view of a die supporting laminate that forms part of the package article of this invention;

5 FIGURE 2 depicts a perspective view of the overmold frame disposed over the laminate illustrated in FIGURE 1;

10 FIGURE 3 shows a perspective view of two heatsink carriers that support optoelectronic dies (e.g., a transmitting, preferably a VCSEL, or optical receiver) and flexible circuits, and which are enclosed within the overmold frame depicted in FIGURE 2;

15 FIGURE 4 illustrates, in perspective view, some of the components of the transceiver as they are positioned relative to one another. One side of the transceiver shows the heatsink carrier with the flexible circuit exiting below it, with the optical coupler not shown, for clarity. The second side of the transceiver shows the carrier secured to the optical coupler. The other side of the transceiver shows the heatsink carrier with the flexible circuit exiting below it and with one end of the flexible circuit in close

20

proximity to one edge of the optoelectronic die, the bonds between the optoelectronic die and the flexible circuit not being shown;

5 FIGURE 5 depicts, in perspective view, the parallel fiber optic cable with optical connectors thereon attached as it would be seen when approaching the receiving bores of the retainers. Attached to one distal end of one of the two elements of the transceiver is one of two optical couplers;

10 FIGURE 6 shows a perspective view of a selection of parts of the package article placed in their proper position along with the optical connectors and parallel fiber optic cable of FIGURE 5. The parts of the transceiver shown are two heatsink carriers, one optoelectronic die, one flexible circuit, the laminate, the overmold frame, one retainer and the faraday barrier shield. Also shown in the overmold frame are features for accepting, in proper alignment, the retainer;

20 FIGURE 7 illustrates in plan view the flexible circuit shown in FIGURES 3, 4 and 6. The depicted flexible circuit has attached thereon a stiffener member and includes as a portion of the flexible circuit a wiring grid of contact

pads which is removed before assembly into the transceiver;
and

FIGURE 8 depicts in cross-sectional view the overall
fabrication of the package article of this invention.

5 For purposes of clarity and brevity, similar elements
and components of the optical assembly of this invention
will bear the same designation and numbering throughout all
the figures.

DESCRIPTION OF THE PREFERRED EMBODIMENT

10 Generally speaking, the invention features a package
for coupling a removable multiple channel parallel fiber
optic cable to a corresponding multiple channel vertical
cavity surface emitting laser (VCSEL) transmitter and for
coupling a removable multiple channel parallel fiber optic
15 cable to a multiple channel perpendicularly aligned
integrated die (PAID) receiver.

The orientation of the fibers of the parallel fiber
optic cable is parallel to the host card to which the

package article is attached. It should be understood that optoelectronic devices may also be configured in arrays or in other predetermined patterns.

5 The transceiver allows for a reduction in the height of the package article by orienting the plane of the optoelectronic dies perpendicular to the plane of the laminate. The vertically oriented transmitting and receiving optoelectronic dies are attached to the horizontally oriented laminate via a flexible circuit.

10 Now referring to FIGURE 1, a laminate 10 horizontally supports wirebond dies 12. These wirebond dies 12 lie parallel to the plane of the laminate surface. One of two optoelectronic dies 18, shown in FIGURE 3, is secured to a heatsink carrier 26 and is oriented perpendicularly with respect to the wirebond dies 12. The plane of the laminate 10 is substantially perpendicular (horizontal) with respect to the vertically oriented optoelectronic dies 18.

20 The overall structure has the advantage that the transceiver 50 (FIGURE 8) has a low profile. Electrical contact pads or balls, not shown, are disposed on the bottom side of the laminate 10 and may be soldered directly to a

host card (not shown) for the purpose of relaying electronic signals from the host card to the electronic components within the transceiver, while fiber optic cables 20 can be plugged directly through the tail stock. In other words,
5 cables 20 (FIGURE 5) exit from the overall transceiver assembly 50 parallel to the horizontal plane of the laminate 10.

An overmold frame 22, shown in perspective view in FIGURE 2, rests on and is bonded to the top surface of
10 laminate 10. The overmold frame 22 can house a faraday barrier shield 24, one half electrically isolating the transmitting section, and the other half electrically isolating the receiving section of the transceiver 50. The electrical isolation serves both to reduce capacitively and
15 inductively coupled high frequency electrical signals, and to reduce the amount of electromagnetic power that is radiated from the transceiver assembly 50.

The overmold frame 22 supports and encloses a heatsink carrier 26, shown in FIGURE 3 and 4, with an attachment
20 optoelectronic die 18. Parallel fiber optic cable 20 (FIGURE 5) is divided into two portions 20a and 20b and is terminated in a parallel fiber optic connector 36. The

connector 36 is inserted into a retainer 38, thereby coarsely aligning the transmitting and the receiving sides of the parallel fiber optic connector 36 with their respective retainer receptacles. After each of the two 5 sides 36a and 36b of the parallel fiber optic connector 36 is partially inserted in a bore 38a and 38b, respectively, of retainer 38, engagement with fine alignment pins, not shown, is made. After full insertion is accomplished, the end faces of each of the two sides 36a and 36b of the 10 parallel fiber optic connector 36 are held in position so that light may pass between the respective elements of the individual optoelectronic devices and the fiber end faces of their corresponding optical fibers within the parallel fiber optic cable 20. Secured to the optoelectronic die 18, and 15 optionally secured to heatsink carrier 26, is a flexible circuit 28.

One function of the flexible circuit 28 is to provide a 20 first attachment end oriented for convenient connection from pads on the laminate 10 to wiring on the flexible circuit 28. This requires that a first end of the flexible circuit 28 must be substantially parallel to laminate 10. Individual wiring channels or leads on flexible circuit 28 are substantially aligned to nearby corresponding terminal

pads spaced out along a terminal pad array 14 on the surface of laminate 10.

A second function of the flexible circuit 28 is to provide a second attachment oriented for convenient 5 connection from pads on the optoelectronic die 18. This requires that a second end of the flexible circuit 28 must be substantially parallel to optoelectronic die 18, where the individual wiring channels (leads) on flexible circuit 10 28 are also substantially aligned to nearby corresponding pads (not shown) on the optoelectronic die 18. The pads on optoelectronic die 18 are electrically attached to the wiring channels on flexible circuit 28, which is mechanically or otherwise secured to heatsink carrier 26. Optionally, there may be electrical connections made from 15 heatsink carrier 26 to wiring channels on flexible circuit 28.

Between its two ends, flexible circuit 28 is bent approximately 90 degrees since optoelectronic die 18 is oriented perpendicular to laminate 10. Thus, during and 20 after connection, parallel fiber optic connector 36 (FIGURE 8) approaches fiber optic coupling assembly 30, shown in FIGURES 6 and 8, parallel to the host card (not shown) to

which laminate 10 of fiber optic coupling assembly 30 is attached.

Referring now to FIGURE 8, the fiber optic coupling assembly 30 consists of an optical coupler 32, a heatsink carrier 26, a flexible circuit 28, and an optoelectronic die 18. Cable termination 34 plugs into the fiber optic coupling assembly. The optical coupler 32 is attached to the heatsink carrier 26. Heatsink carrier 26 has optoelectronic die 18 attached thereon. Attachment is accomplished by a thermally and electrically conductive adhesive, not shown. Retainer 38 holds optical coupler 32. One distal end of each optical coupler 32 faces a corresponding optoelectronic die 18; the other distal end of each optical coupler 32 faces the optically terminated end faces of the respective connectors 36a and 36b. The optical coupler 32 is substantially enclosed within retainer 38 (FIGURE 5), so that one end of optical coupler 32 protrudes from one end of retainer 38, while the other end of optical coupler 32 is contained within the region enclosed by retainer 38. Each of the two portions of retainer 38 also accepts one respective fiber optic connector 36. The end face of each of the parallel fiber optic connectors 36 is accurately positioned within its corresponding retainer 38

relative to the end face of the optical coupler 32, which is also secured within retainer 38.

Each portion of the parallel fiber optic connector 36 consists of an optical ferrule having alignment features for 5 accurately aligning with the optical coupler. Protective housing 36' contains connectors 36a and 36b that are coarsely aligned to the respective receptacle 38a and 38b of retainer 38, firmly holding the connector 36 partially within its retainer 38. The means by which the connector 36 10 is held in the retainer 38 is a latching mechanism (e.g., RJ), consisting of snap-in features molded into the protective housing 36' and into the retainer.

The retainers and attached coupler subassembly 30 are supported within the overmold frame 22, as shown in FIGURES 15 6 and 8.

Referring again to FIGURE 1, the laminate 10 comprises grounding pads 16 and terminal pads 14. Grounding pads may be used for electrically coupling to the heatsink carrier. They may also be used for electrically coupling to the 20 faraday barrier shield. Terminal pads 14 are used for electrically coupling to the leads on flexible circuit 28

(FIGURE 4).

Referring now to FIGURE 3, the optoelectronic dies 18 (GaAs, for example) are placed onto heatsink carrier 26. Optical coupler 32 is attached to locating features on 5 heatsink carrier 26 so that the optically active areas of the optical coupler 32, namely the individual fiber end faces, are accurately aligned to the optically active areas of the optoelectronic die 18. The pattern of active areas is shown to be linear for purposes of this description, but 10 it should be understood that any other array configuration or other predetermined pattern could also be incorporated. Such active areas are individual VCSELs on the transmitter side and individual PAID photodetectors on the receiver side.

15 To aid in the assembly and alignment of carrier 32 and heatsink carrier 26, carrier holes 41 are provided to accept pins 40 from coupler 32, not shown in FIGURE 3.

Heat removal from the optoelectronic dies 18 is facilitated by heatsink carrier 26, which provides a low 20 thermal resistance path for heat flow from optoelectronic dies 18 to the package cover 25 (FIGURE 8), which assists in

removing heat from the package article. The heatsink carrier 26 may be made of die cast zinc or molded aluminum, magnesium or copper. The base metal of the heatsink carrier 26 can be plated or treated with various metals, such as 5 zinc and nickel, and may have a final plating of gold. The gold may be selectively coated for the purpose of enhancing the attachment of the channel wiring of flexible circuit 18. One method of attachment is single point tape automated bonding. The heatsink carrier 26 may be constructed using a 10 variety of known techniques such as forming, stamping, coining and molding.

The optical subassembly is positioned upon the overmold frame 22 using alignment features molded into both retainer 38 and overmold frame 22. The flexible circuit 28 extending 15 from the optical subassembly is bent to follow the shape of the curved lower part of the heatsink carrier 26, thereby redirecting the path of the flexible circuit 28 so that its end lies substantially parallel to and in contact with the surface of laminate 10.

20 Near the distal, exposed end of the flexible circuit 28, an electrical connection is made to the terminal pads 14 on the surface of laminate 10. This electrical connection

may be wirebonded, tape automated bonded, or bonded by other means commonly practiced in the industry. The heatsink carrier 26 may also be bonded, using an electrically conductive adhesive, to grounding pads 16 disposed on the 5 surface of laminate 10.

Alternatively or in addition, faraday barrier shield 24 (FIGURE 2) may be attached to grounding pads 16. Faraday barrier shield 24 may also be disposed in physical and electrical contact with a package cover 25 (FIGURE 6).

10 Cover 25 can also be used as a heatsink for heat transferred from optoelectronic die 18 via heatsink carrier 26. The various connections to the grounding pads 16 may be accomplished using conductive epoxy, a material that conforms to the electrically conductive and nonconductive 15 portions of the various surfaces, thereby reducing the necessity of building these components to high dimensional and positional tolerances.

A thermal path, separate from the one described hereinabove for the optoelectronic dies, is provided from 20 the wirebond dies 12 through the ball grid array laminate 10 and through the solder balls on the bottom surface thereof to the host card on which the package assembly is mounted.

Alternatively, the laminate 10 may be constructed without solder balls but with an array of pads for connection to a receiving socket (not shown).

One layer of the flexible circuit 28 comprises a copper 5 layer with electrical traces (e.g., .002" lines and .002" spaces) on a polyimide carrier, with solder mask material selectively covering a portion of the copper layer. The copper wiring may be coated with a less chemically reactive metal, such as gold or tin. Multiple layer flex circuits 10 may also be used with thru-via interlayer connections and grid planes to provide higher density and performance.

Windows may be provided through the polyimide carrier to facilitate electrical connection to some of the leads of the copper wiring. The flexible circuit 28 has an open 15 window with suspended leads spanning the window area. Some suspended leads may be electrically coupled to the metal heatsink carrier 26. The leads of the flexible circuit may also be cantilevered in the window region and/or may extend from the end of the flexible circuit. Those cantilevered 20 leads may be electrically coupled to the heatsink carrier 26. Suspended and cantilevered leads may be alternately formed in the window region and selectively electrically

coupled to a prepared site on the heatsink carrier 26. Electrically coupling flexible circuit leads to the heatsink carrier 26 can be accomplished by bonding under conditions of heat and ultrasound by the single point tape automated 5 bonding process, which is well known in the electronics packaging industry.

Alignment of the optical coupler 32 to the heatsink carrier 26 is accomplished by inserting small pins 40 (FIGURE 8), which are part of the optical coupler 32, into 10 oversized holes (not shown) which are formed in the heatsink carrier 26. This arrangement provides rough alignment between the two parts 26, 32. The annular gap between the pins 40 and their corresponding holes is filled with UV curing adhesive (epoxy). After curing under ultraviolet 15 light, the epoxy rigidly holds optical coupler 32 in its aligned position.

Immediately before UV curing begins, the active portions of optoelectronic die 18 are aligned accurately 20 with the corresponding optically active portions of optical coupler 32. This alignment process is accomplished using a process known as active alignment, in which the optoelectronic dies 18 are powered up, as appropriate, for

either the transmitting or the receiving optoelectronic die 18, by applying electronic signals to the large two-dimensional pad array 29 at one end of the flexible circuit 28 (FIGURE 7). Optical signals pass through an optical cable, not shown, temporarily attached to the connector end of the optical coupler 32, and monitor optoelectronic devices, not shown, that are connected to the other end of this temporarily attached optical cable.

The electronic signals to and from the monitoring optoelectronic device are used during the alignment process. For example, to align a transmitter optoelectronic die 18, electronic signals are applied to the large pads of the flexible circuit 28, while the optical coupler 32 is held by a positioning device against the heatsink carrier 26. The optical coupler pins 40, oppositely disposed to the pins 40 shown on FIGURE 8, are inserted in the oversized carrier holes 41 of the heatsink carrier 26, the UV curable adhesive being disposed in the gap between the pins and the holes 41. The diameter of carrier holes 41 is greater than the diameter of the corresponding pins. One or more of the optically active elements of the transmitter optoelectronic die 18, previously mounted on the heatsink carrier 26 and in electrical contact with the wiring on the flexible circuit

28, is thereby activated. Light emerges from the optically active elements of the transmitter optoelectronic die 18. The light of individual, optically active elements passes into corresponding optically active portions of the optical coupler 32. The light passes through the optical coupler 32 and emerges from the distal end thereof at corresponding optically active portions. Then the light passes into the temporary parallel fiber optic cable at corresponding optically active portions, travels through the cable, and emerges onto corresponding photodetectors, not shown.

10 Accordingly, the light emerging from each optically active element of the transmitter optoelectronic device 18 is substantially conveyed to a corresponding photodetector. The alignment task is accomplished by moving the relative position of the optical coupler 32 to the optoelectronic die 18, using the positioning device, so that an optimized amount of light is coupled, as determined by measuring the current generated within the associated photodetectors.

15

Silicone or optically transparent UV curable epoxy can be applied between optoelectronic die 18 and optical coupler 32 to provide passivation of surfaces of optoelectronic die 18. This material also provides an optical path which does not emerge into air. This is advantageous because

relatively large losses of optical power are incurred, due to Fresnel reflections, at each entrance into or egress from air.

5 Split power and ground planes, not shown, disposed in the laminate 10 control noise and reduce crosstalk between the transmitter and receiver portions of the optoelectronic die 18.

10 A split heatsink cover, not shown, may be used to control electromagnetic interference (EMI). The heatsink cover can be stamped for economy. Its function is to provide for heat dissipation. The heat dispersed by this heatsink cover is generated substantially at the optoelectronic die 18, and is conveyed by thermal conduction to heatsink carrier 26 and on to the package cover 25, where 15 any gap between the heatsink carrier 26 and the package cover 25 is filled, optionally, with a heat conducting compound, well known in the art.

20 While the present invention has generally been described with respect to transmitting operations, it should be understood that the inventive device is equally well suited to handle receiving operations, as well. In other

words, when transmitting, the package article accepts signals from the host card and generates corresponding high-current signals for transmitting to the optoelectronic die; on the receiving side, the package article accepts low-current signals generated by the optoelectronic die and amplifies and digitizes the received signals to levels suitable for transmitting to the host card.

Since other modifications and changes varied to fit particular operating requirements and environments will be apparent to those skilled in the art, the invention is not considered limited to the example chosen for purposes of disclosure, and covers all changes and modifications which do not constitute departures from the true spirit and scope of this invention.

Having thus described the invention, what is desired to be protected by Letters Patent is presented in the subsequently appended claims.

What is claimed is: